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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NO.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10/052924	01/18/2002	339	185.19	2652	HOANG HUANG

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365 288

**CONTINUING DATA VERIFIED: *lu*

**FOREIGN APPLICATIONS VERIFIED: *lu*

PG-FUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		M-11821 US
Verified and Acknowledged Examiners's initials <i>lu</i>		
TITLE: Noise reduction technique for transistors and small devices utilizing an episodic agitation		

U.S. DEPT. OF COMM. PAT. & TM. PTO-436 (Rev. 12-94)

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<p>8-26-03 <i>lu</i></p> <p>ISSUE FEE</p> <p>Amount Due _____ Date Paid _____</p> <p>(Assistant Examiner) _____ (Date) _____</p>			<p>NOTICE OF ALLOWANCE MAILED</p> <p>ISSUE FEE</p> <p>Amount Due _____ Date Paid _____</p>	
<p>TERMINAL <input type="checkbox"/> DISCLAIMER</p> <p>Formal Drawings (9/18/03) Set</p>			<p>ISSUE BATCH NUMBER</p>	